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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Akira YOSHIZAWA, et al.

Application No.: 09/970,593

Confirmation No.: 1195

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Group Art Unit: 2826

Examiner: Tan N. Tran

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For: SEMICONDUCTOR DEVICE USING INTERPOSER SUBSTRATE AND MANUFACTURING METHOD THEREFOR

SUBMISSION OF CORRECTED FORMAL DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please find attached one (1) sheet of corrected formal drawings for Figure 11, which now includes the legend --Prior Art--. The Examiner is respectfully requested to acknowledge receipt of the corrected formal drawing.

Respectfully submitted,

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